

Proceedings of the 29th Annual Ieee/Acm International Symposium on Microarchitecture Micro-29: December 2-4, 1996 Paris, France



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Efficient path profiling - ACM Digital Library - Association for The instruction segment may include instructions or uops (micro-instructions). A trace is perhaps the most common type of instruction segment. Typically, a trace **dblp:**

MICRO Patent WO1999031576A1 - Combining multiple class files into run Proceedings of the 8th Annual International Symposium on Computer . Barcelona, Spain (June 29-July 1, 1998), pp. Annual IEEE/ACM International Symposium on Microarchitecture (MICRO 32), Proceedings of the 29th Annual International Symposium on Microarchitecture, Paris, France (December 2-4, 1996), pp. **Proceedings of the 29th annual IEEE/ACM International Symposium** ANNUAL IEEE/ACM INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. MICRO-29. PARIS, DEC. 2 - 4, 1996, PROCEEDINGS OF THE ANNUAL **Exceeding the dataflow limit via value prediction - ACM Digital Library** Proceedings of the 29th Annual Ieee/Acm International Symposium on Microarchitecture Micro-29: December 2-4, 1996 Paris, France [International Symposium **Patent EP1387277A3 - Write back policy for**

- Dec 2, 1996 MICRO 29 Proceedings of the 29th annual ACM/IEEE international symposium on Microarchitecture. Pages 46-57. Paris, France December **Big Data Analytics and Knowledge Discovery: 17th International - Google Books Result** ANNUAL IEEE/ACM INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. MICRO-29. PARIS, DEC. 2 - 4, 1996, PROCEEDINGS OF THE ANNUAL **Patente EP1387277A3 - Write back policy for**

memory - Google Sep 1, 1991 MICRO 24 Proceedings of the 24th annual international symposium on .. pipeline, Proceedings of the 29th annual ACM/IEEE international symposium on Microarchitecture, p.14-23, December 02-04, 1996, Paris, France. **Patent US7802077 - Trace indexing via trace end** - In Proceedings of the 29th Annual International Symposium on Microarchitecture, pages 58-67, Paris, France, Dec. 2-4, 1996. IEEE Computer Society TC- **Proceedings of the 29th Annual Ieee/Acm International Symposium** Thus, the instructions may be stored as

microinstructions (commonly, uops). . Cache Fetch Mechanism, Proceedings of the 30th annual ACM/IEEE international symposium on Microarchitecture, Dec. 2-4, 1996, Paris, France. 29, Sez nec et al, Multiple-Block Ahead Branch Predictors, Proceedings of The 7th Intl. **dblp: Steven J. Beaty** Dec 2, 1996 MICRO 29 Proceedings of the 29th annual ACM/IEEE international symposium on Microarchitecture Paris, France December 02 - 04, 1996 . annual international symposium on Microarchitecture, p.10-19, December **Two-level adaptive training branch prediction - ACM Digital Library** ANNUAL IEEE/ACM INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. MICRO-29. PARIS, DEC. 2 - 4, 1996, PROCEEDINGS OF THE ANNUAL **Limits of Parallelism** 29TH. ANNUAL IEEE/ACM INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. MICRO-29. PARIS, DEC. 2 - 4, 1996, PROCEEDINGS OF THE ANNUAL IEEE/ACM INTERNATIONAL 29, 2 December 1996 (1996-12-02), pages 214 - 225, XP000724373, ISBN: Owner name: TEXAS INSTRUMENTS FRANCE. **This work has been submitted to the IEEE for possible publication in** 17th International Conference, DaWaK 2015, Valencia, Spain, September 1-4, 2015, Proceedings of the 29th Annual IEEE/ACM International Symposium on Microarchitecture, MICRO 29, Paris, France, 24 December 1996, pp. 4657. **dblp: MICRO 1996** ANNUAL IEEE/ACM INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. MICRO-29. PARIS, DEC. 2 - 4, 1996, PROCEEDINGS OF THE ANNUAL **Proceedings of the 29th Annual IEEE/ACM International Symposium** Apr 8, 2016 Proceedings of the 29th Annual IEEE/ACM International Symposium on Microarchitecture, MICRO 29, Paris, France, December 2-4, 1996. **Proceedings of the Ninth Annual ACM-SIAM Symposium on Discrete - Google Books Result** 29TH ANNUAL IEEE/ACM INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. MICRO 29, PARIS, FRANCE, 2-4 DEC. 1996, ISBN 0-8186-7641-8, **Patent WO1998027486A1 - Method and apparatus for storing and** Proceedings of the 29th Annual IEEE/ACM International Symposium on Microarchitecture : MICRO 29, December 2-4, 1996, Paris, France, sponsored by IEEE Computer Society Technical Committee on Microprogramming and **Method and apparatus for sampling multiple potentially concurrent** Proceedings of the 29th annual IEEE/ACM International Symposium on Microarchitecture, MICRO-29 : December 2-4, 1996, Paris, France. sponsored by IEEE **Patent EP1387277A3 - Write back policy for** - 41st Annual IEEE/ACM International Symposium on Microarchitecture Symposium on Microarchitecture (MICRO-40 2007), 1-5 December 2007, 29. MICRO 1996: Paris, France. MICRO 29, Proceedings of the 29th Annual IEEE/ACM International Symposium on Microarchitecture, December 2-4, 1996, Paris, France **The Performance Potential of Data Dependence - melco** ANNUAL IEEE/ACM INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. MICRO-29. PARIS, DEC. 2 - 4, 1996, PROCEEDINGS OF THE ANNUAL ACM, Dec 2000. IEEE Journal of Solid-State Circuits, 27(4):473484, 1992. [5] Intel In Proceedings 25th Annual Annual International Symposium on Computer Architecture, Barcelona, Spain, June 1998. 29th Annual International Symposium on Microarchitecture, pages 142152, Paris, France, December 24, 1996. **IEEE Computer Society Technical Committee on Microprogramming** Proceedings of the 29th Annual IEEE/ACM International Symposium on Microarchitecture, MICRO 29, Paris, France, December 2-4, 1996. ACM/IEEE Computer **Patent US7802077 - Trace indexing via trace end - Google** Sep 21, 2010 Thus, the instructions may be stored as microinstructions (commonly, US6073213, Dec 1, 1997, Jun 6, 2000, Intel Corporation, Method annual ACM/IEEE international symposium on Microarchitecture, Instruction Fetching, Proceedings of the 29th Annual IEEE/ACM Intl. 2-4, 1996, Paris, France. **Patent WO2001097041A3 - Compiler-based cache line optimization** Copyright 1996 IEEE. Published in the Proceedings of the 29th Annual ACM/IEEE International Symposium on Microarchi-. tecture, Dec. 2-4, 1996, Paris France. Personal use of this material is permitted. of the 25th Annual ACM/IEEE International. Symposium and Workshop on Microarchitecture, September. 1992. **Patente EP1387277A2 - Write back policy for memory - Google** to the IEEE for possible publication in the Proceedings of the 29th Annual International. Symposium on Microarchitecture, December 2-4, 1996, Paris, France.