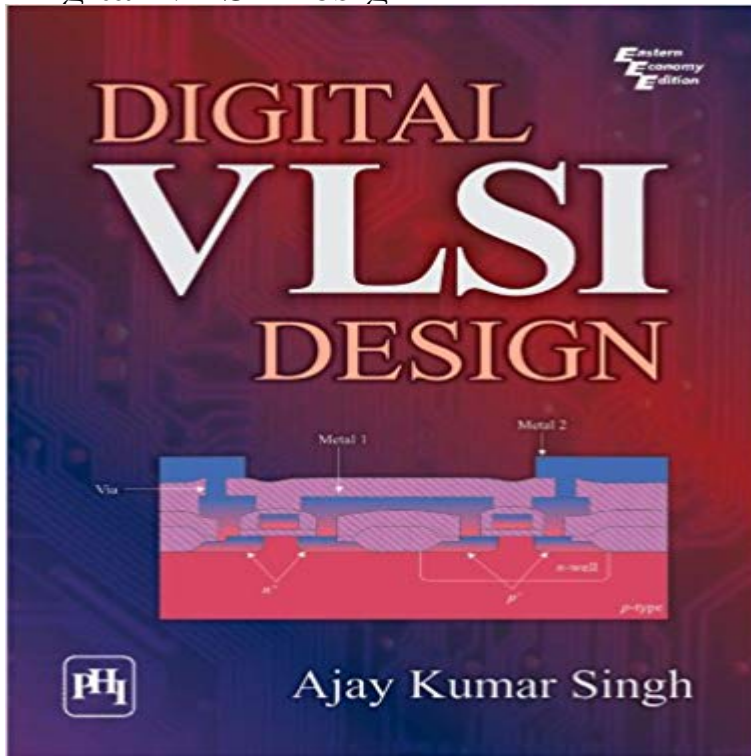


Digital VLSI Design



This well-organized book provides an in-depth coverage of VLSI design engineering, which ranges from CMOS logic to physical design automation. The book begins with a discussion on the structure and operation of MOS as MOSFET is the basic building block for any VLSI design. Then, it goes on to explain the various fabrication methods of MOSFET and CMOS, implementation and properties of MOS inverter circuit, and parasitic parameters and resistances associated with MOSFET, which determine and ultimately limit the performance of a digital system. Besides, it describes design methodology and the concept of the combinational static logic circuits, sequential circuit design and CMOS dynamic circuits. Finally, the book examines semiconductor memory and the importance of adder and multiplier circuits for the VLSI designer. Primarily intended as a text for the undergraduate and postgraduate students of Electrical and Electronics Engineering, the book would also be of considerable value to designers both beginners and professionals. KEY FEATURES Provides mathematical derivations for both noise margin and logic voltage. Explains all combinational and sequential logics separately. Contains a large number of solved and unsolved problems based on issues related to digital VLSI design.

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